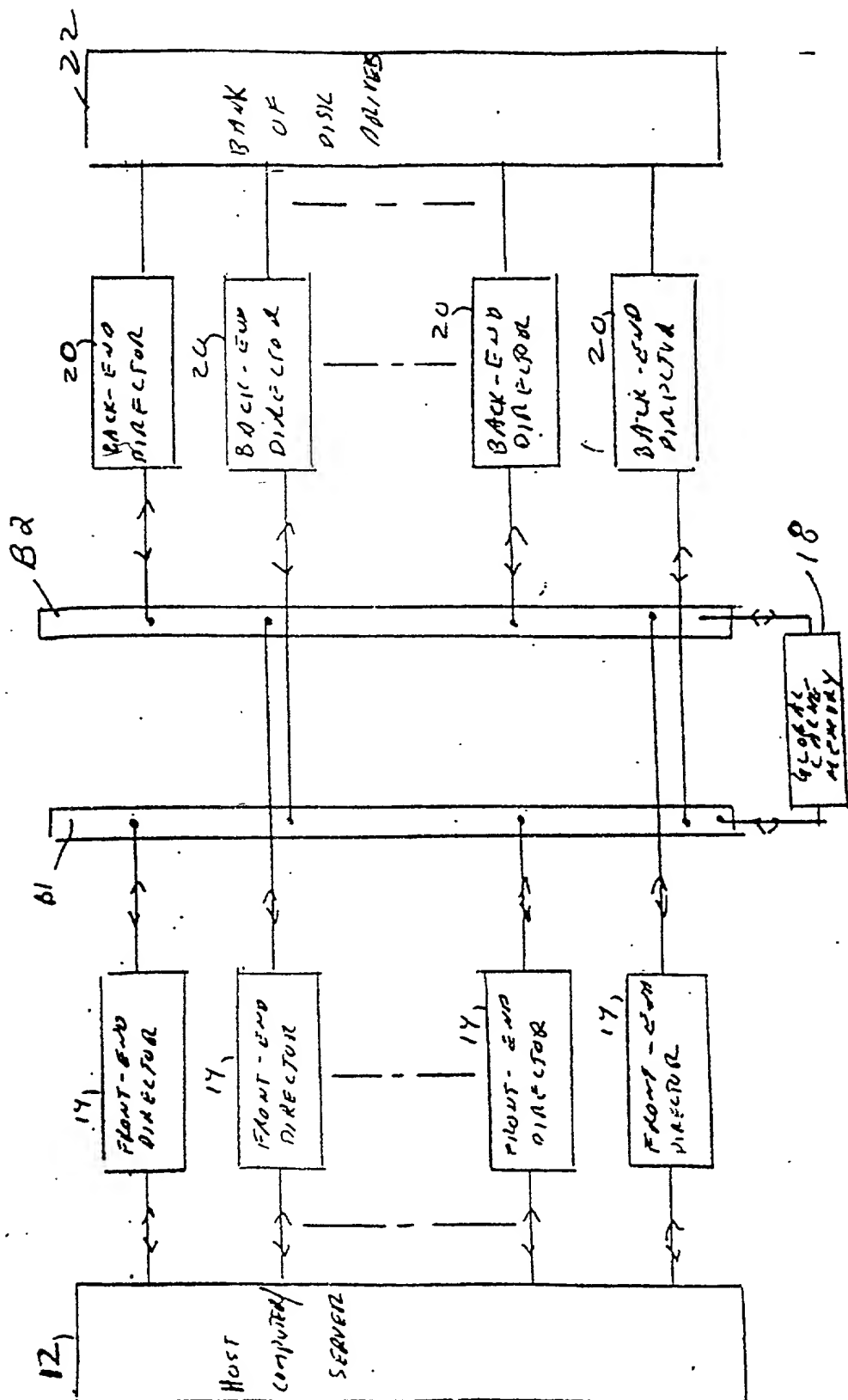
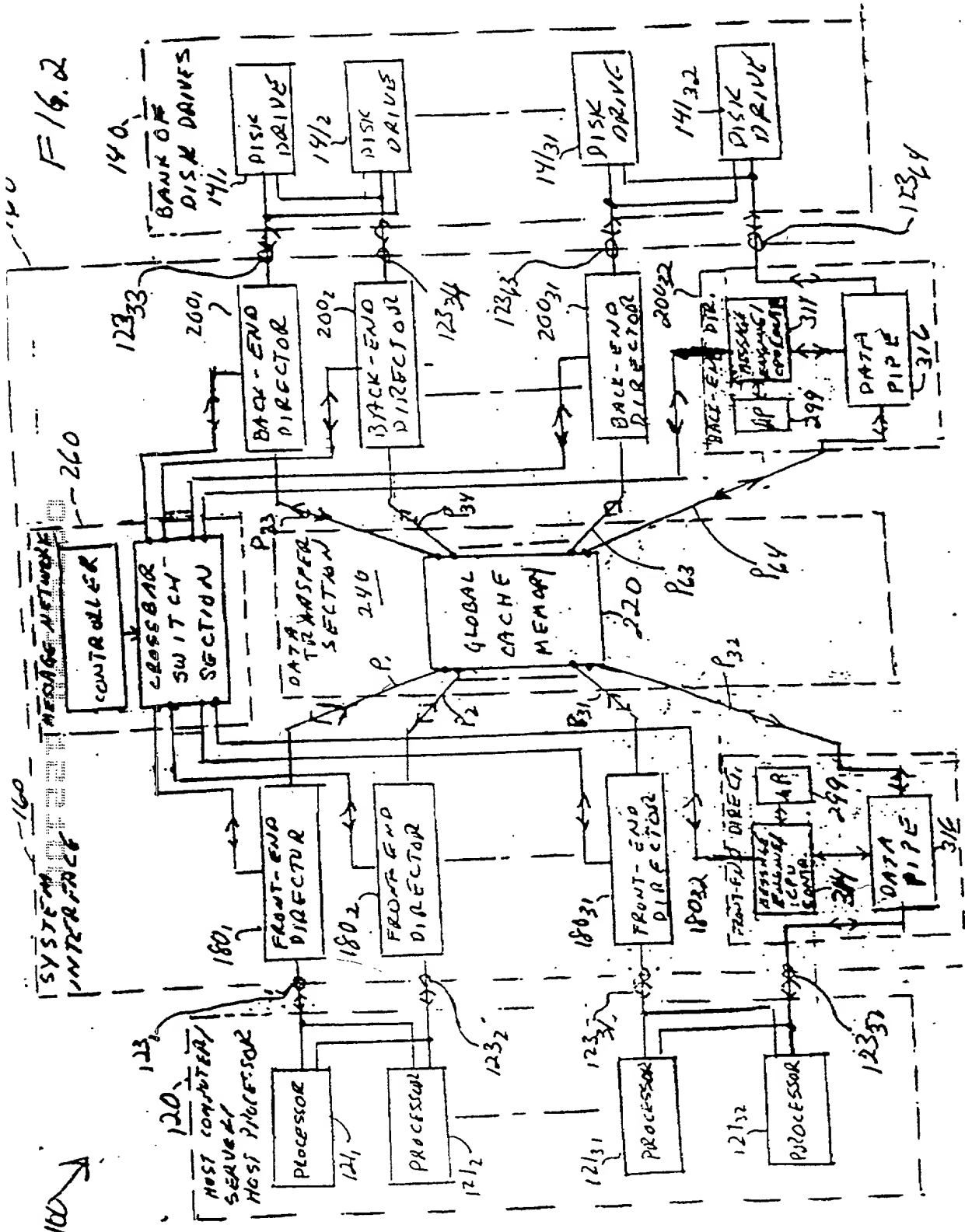


FIG. 1
PARA 125





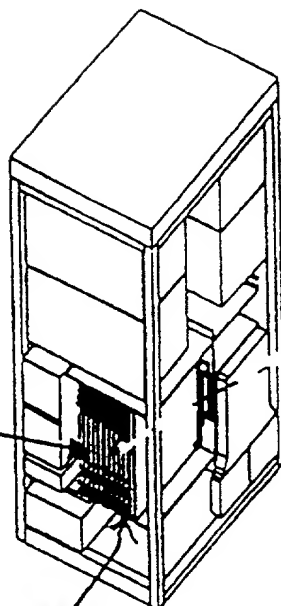
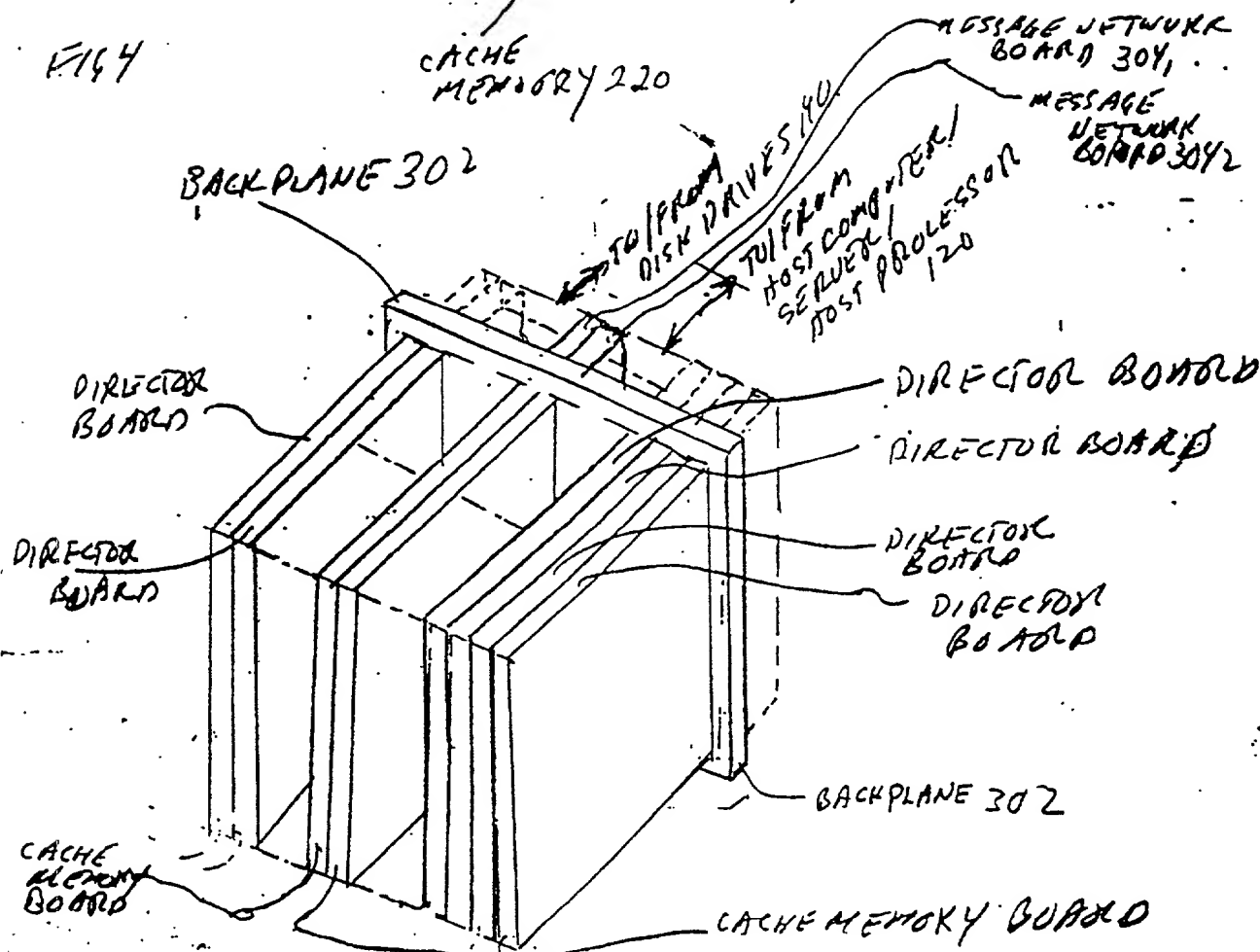
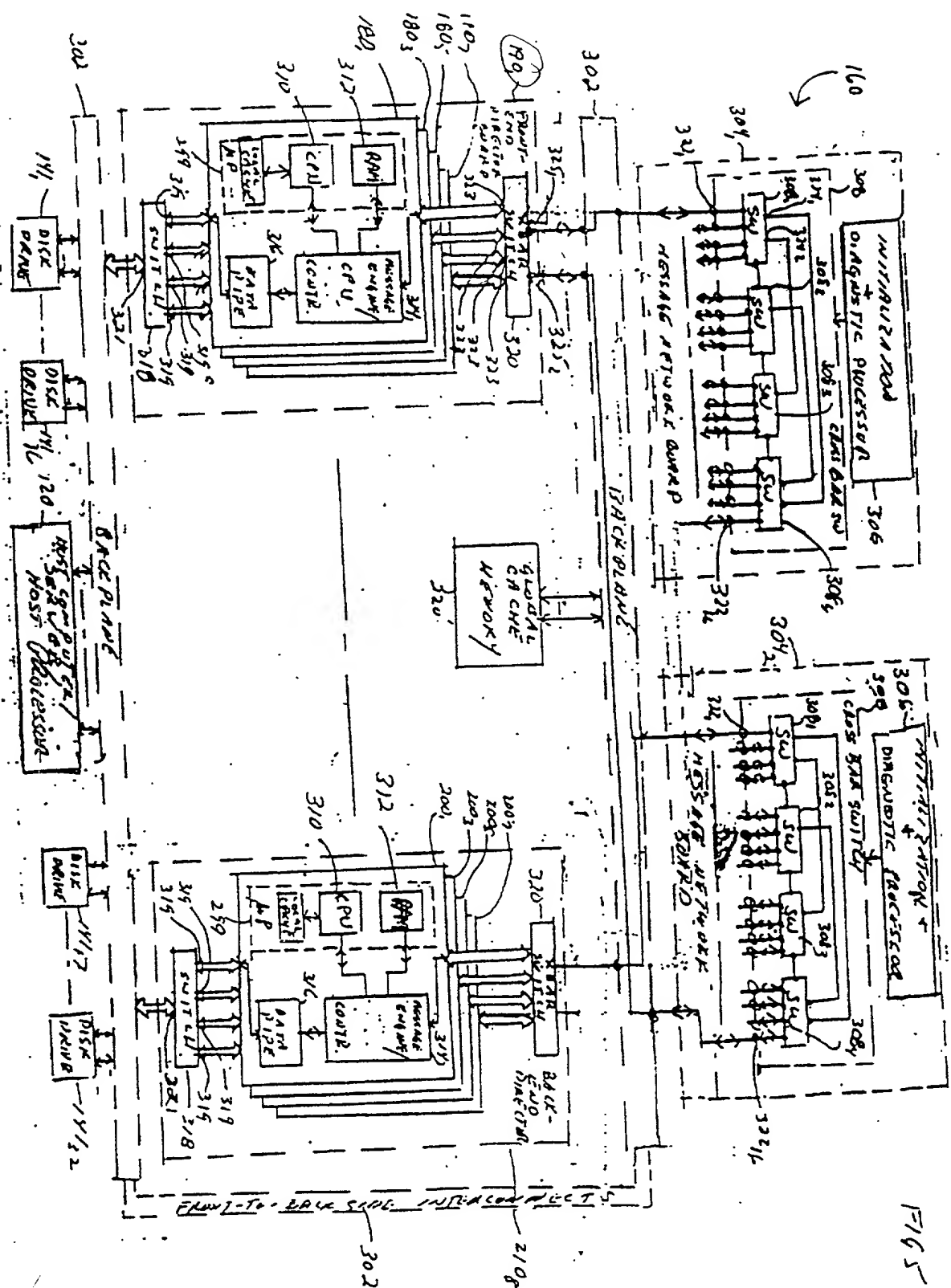


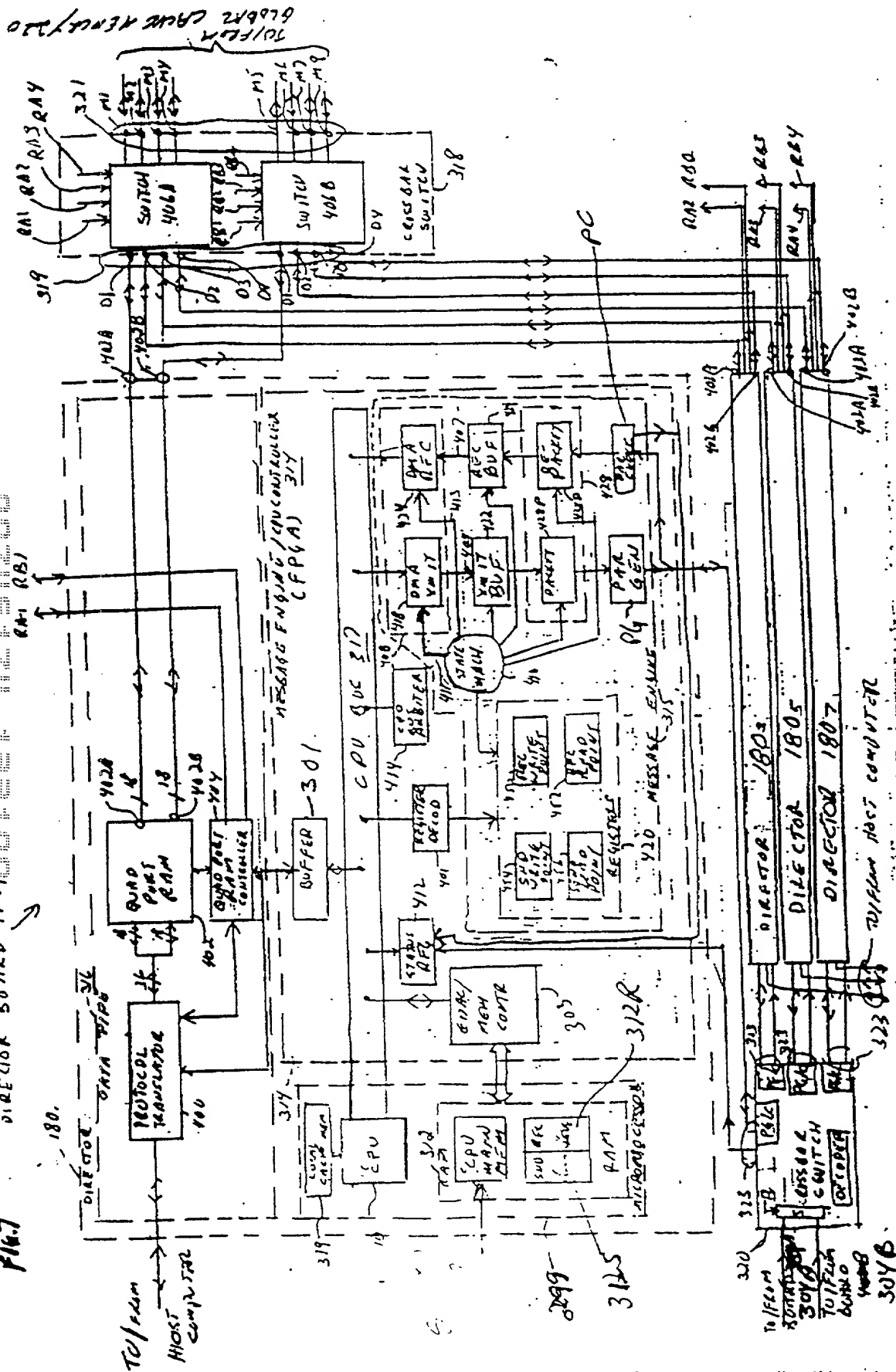
FIG. 3

FIG. 4





DIRECTOR BOARD 1901



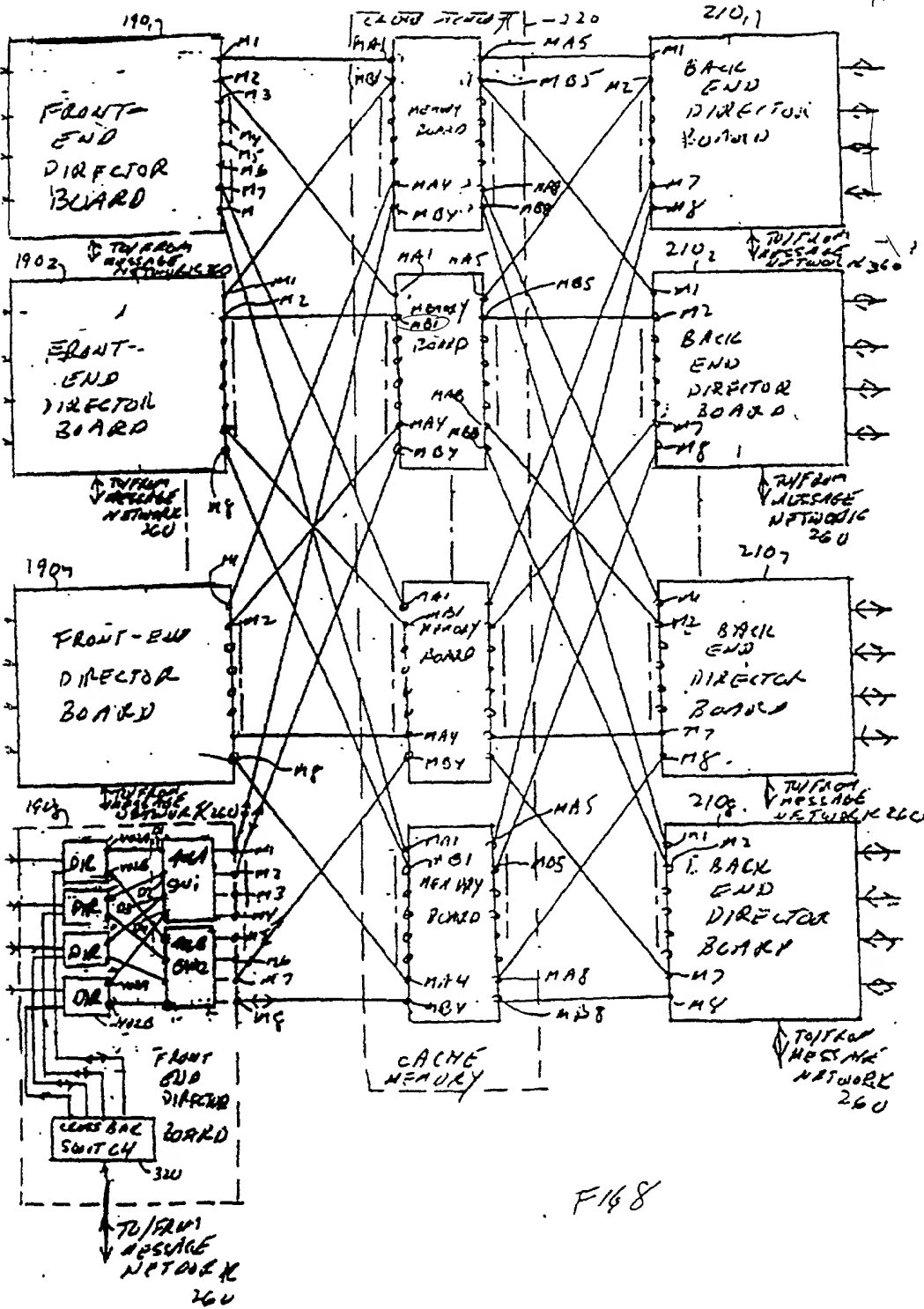
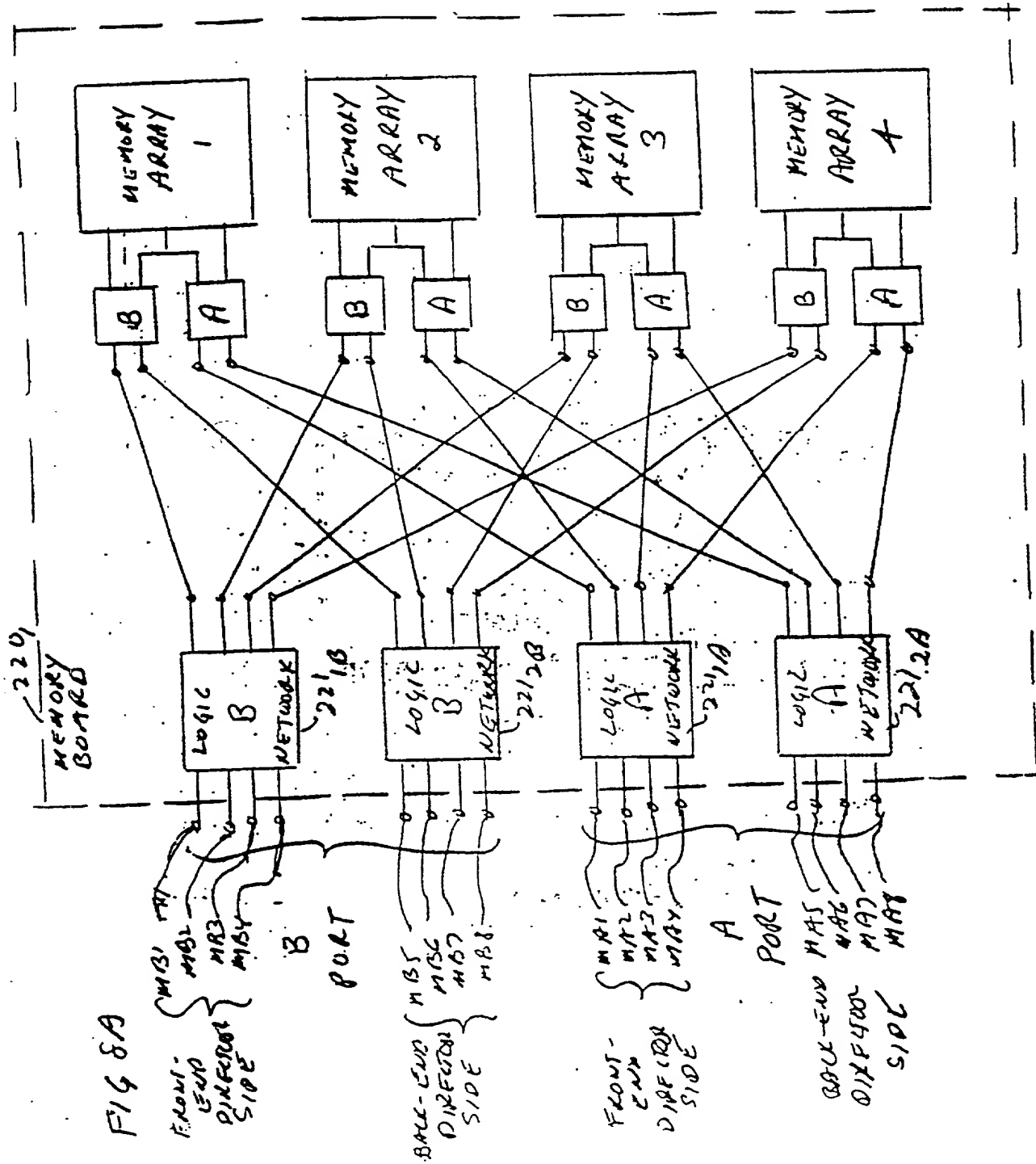
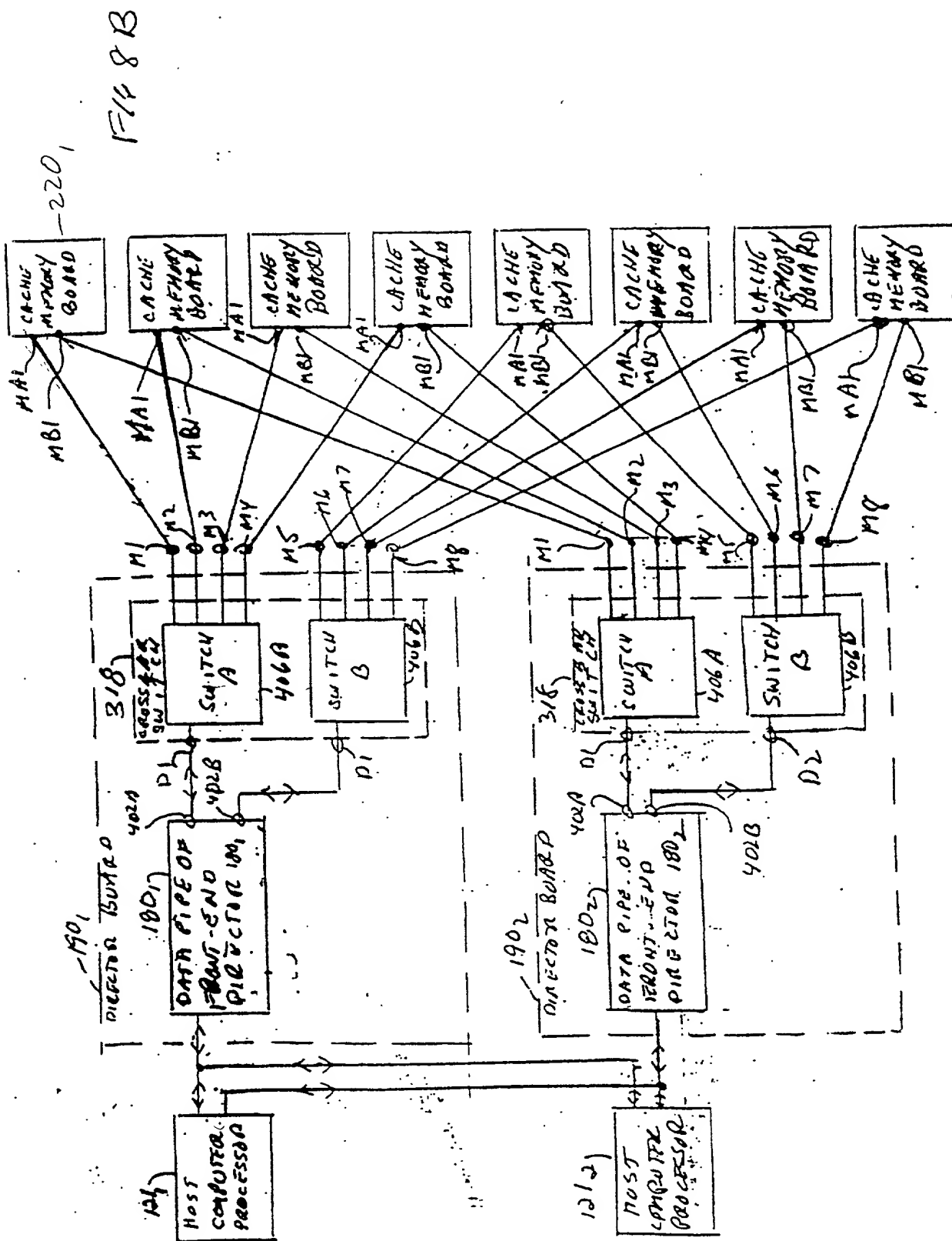


FIG 8

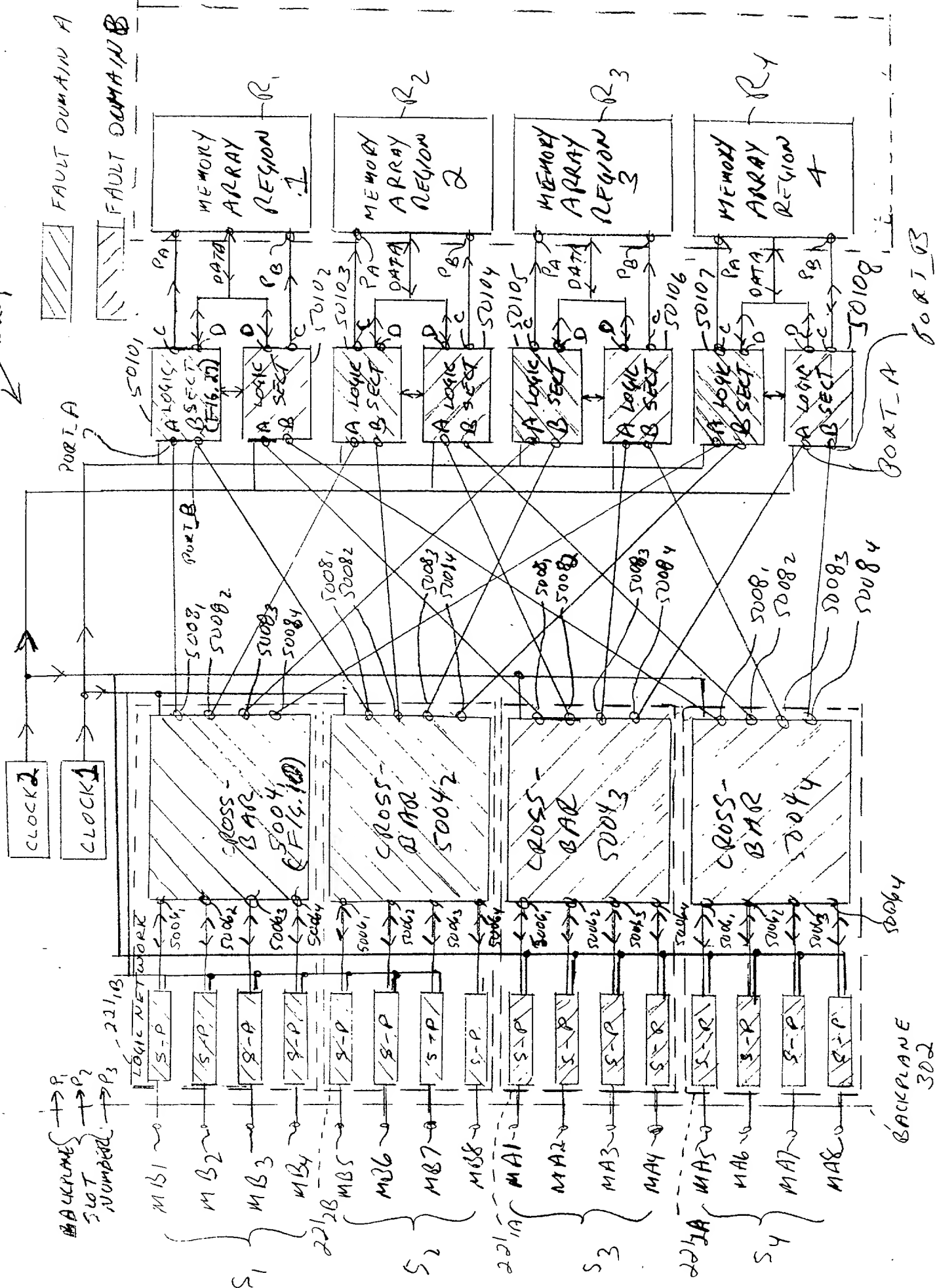
Fig 8A



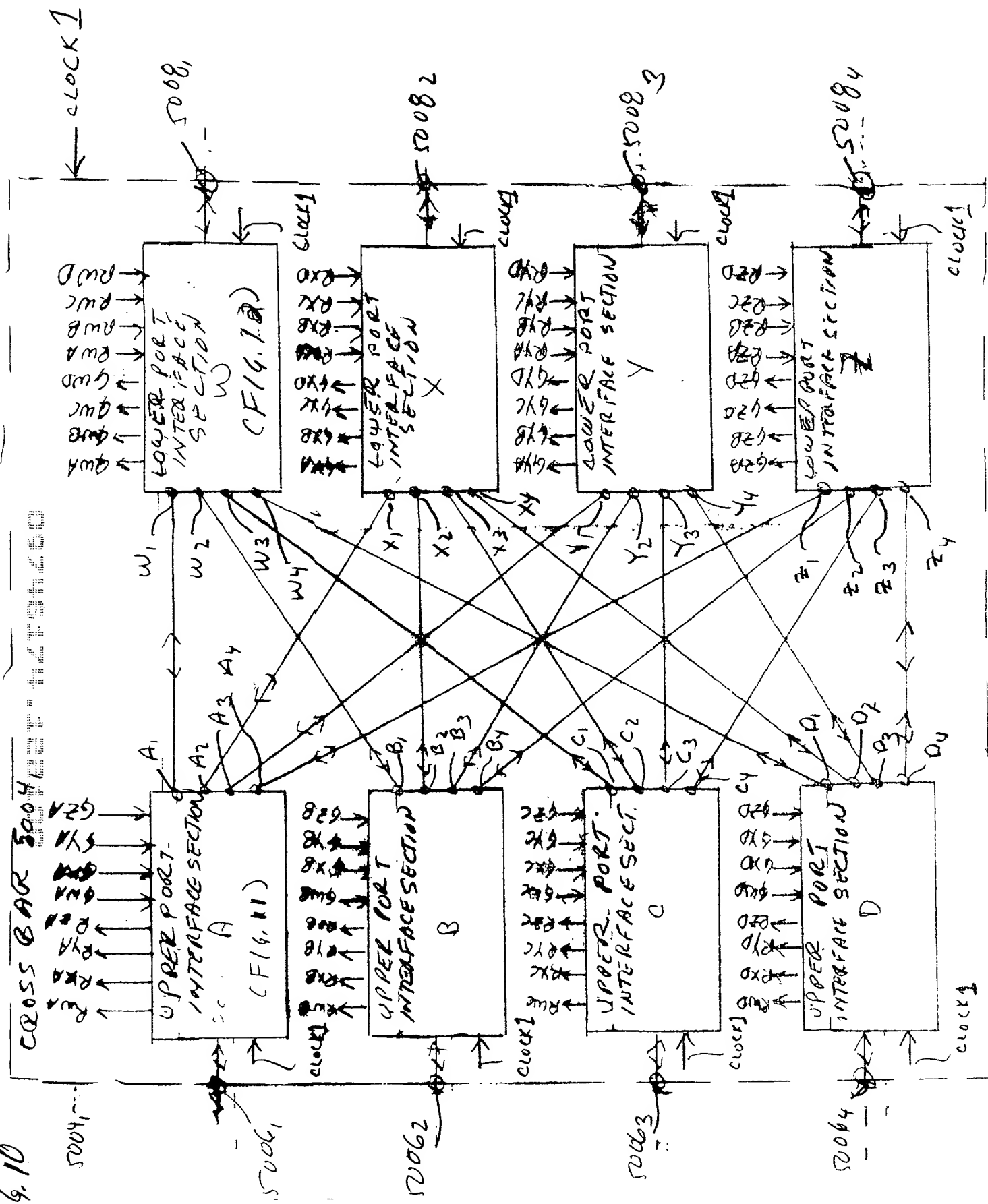


[illegible]

$\begin{matrix} \rightarrow p_1 \\ \rightarrow p_2 \\ \rightarrow p_3 \end{matrix} \left\{ \begin{array}{l} \text{Axioms} \\ \text{Set} \\ \text{Number} \end{array} \right.$



CLASS BAR 5004



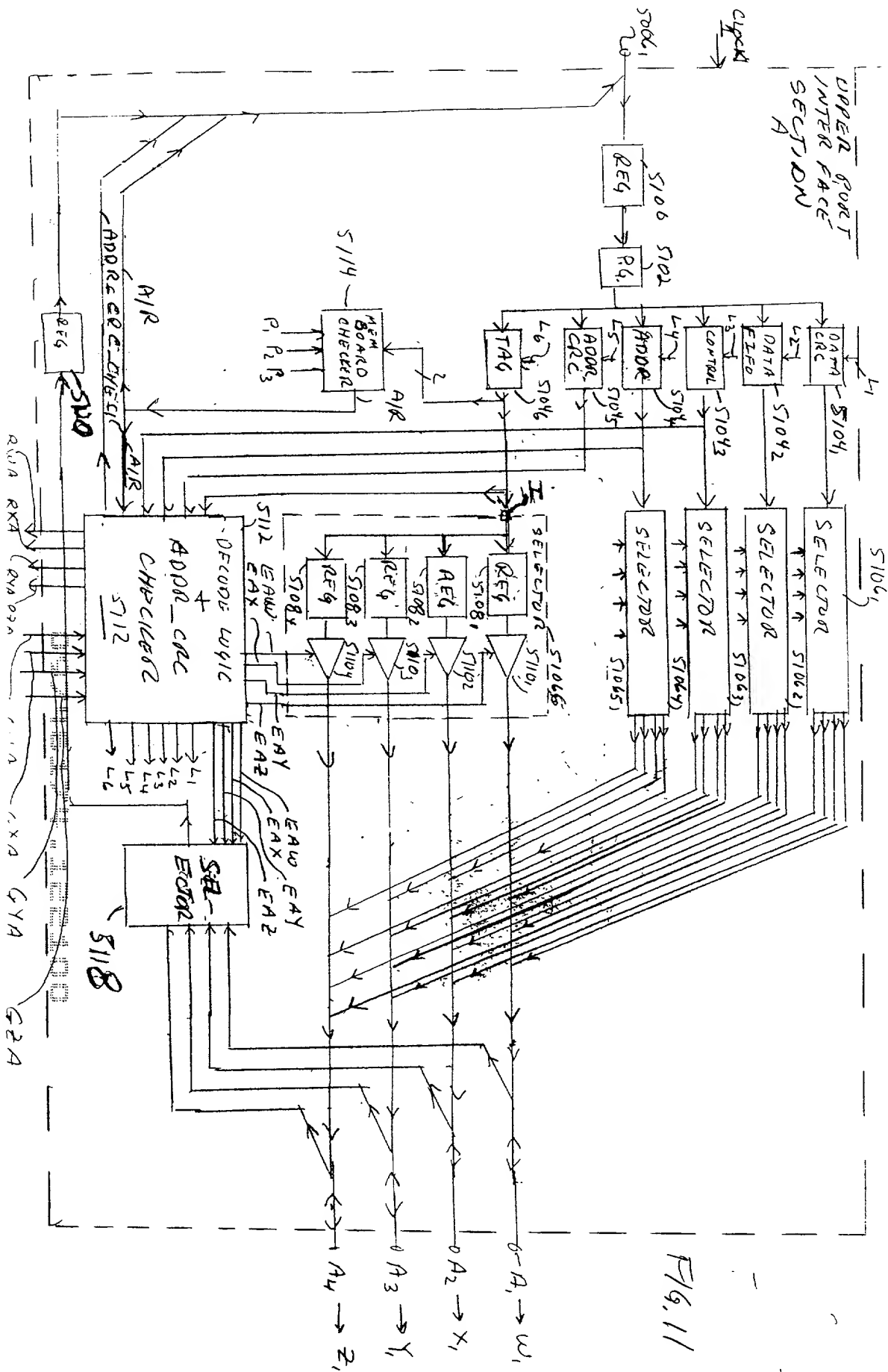
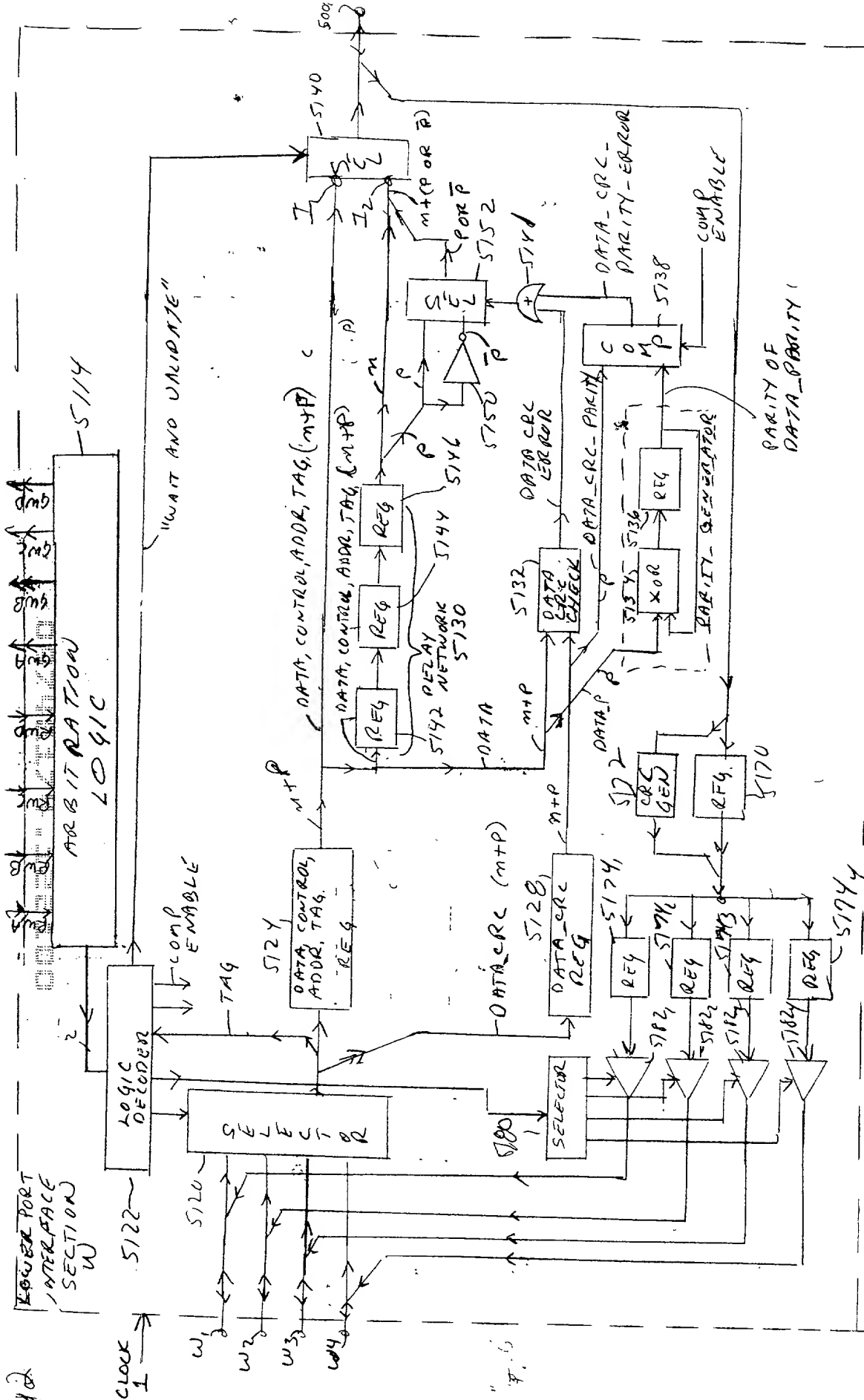
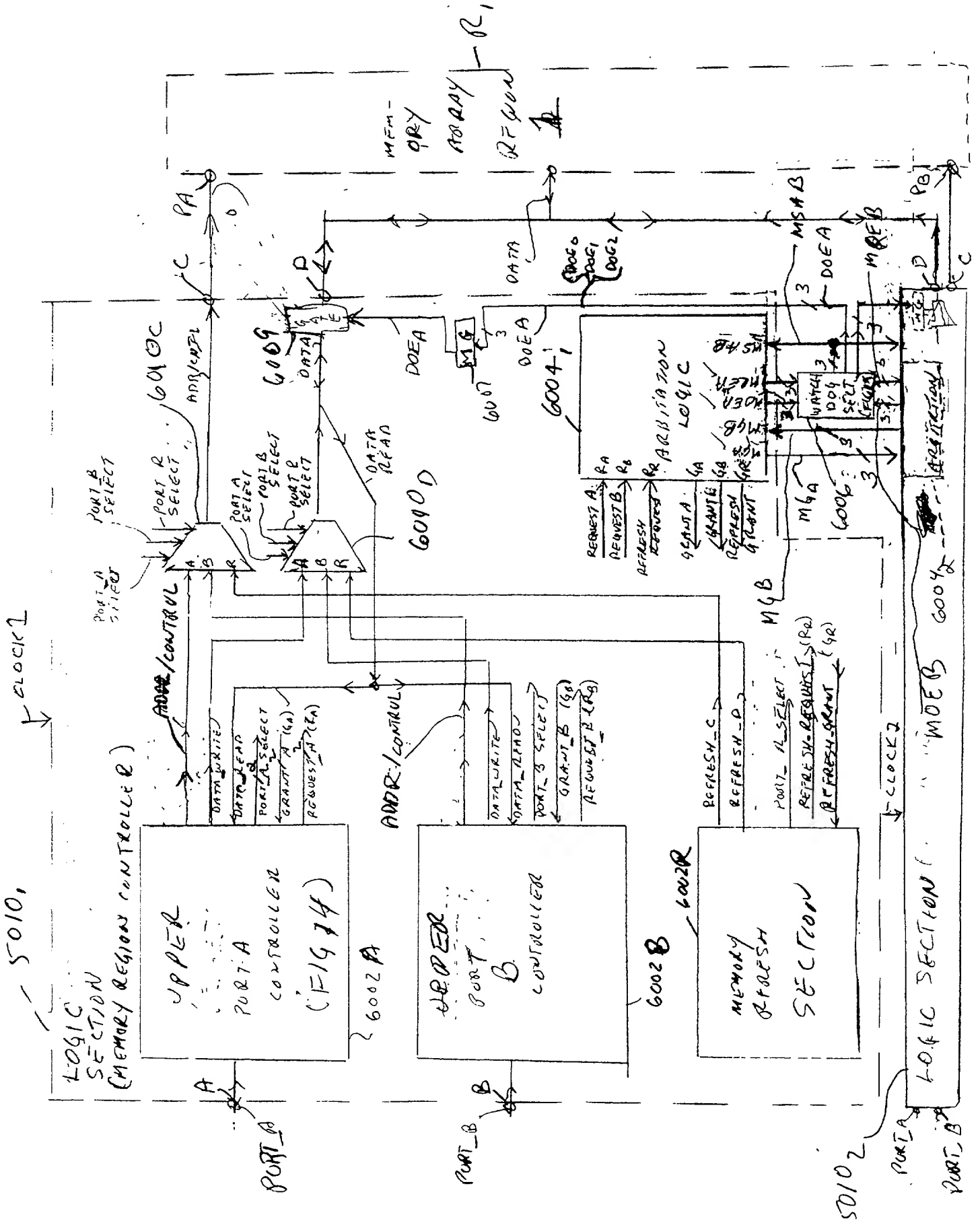
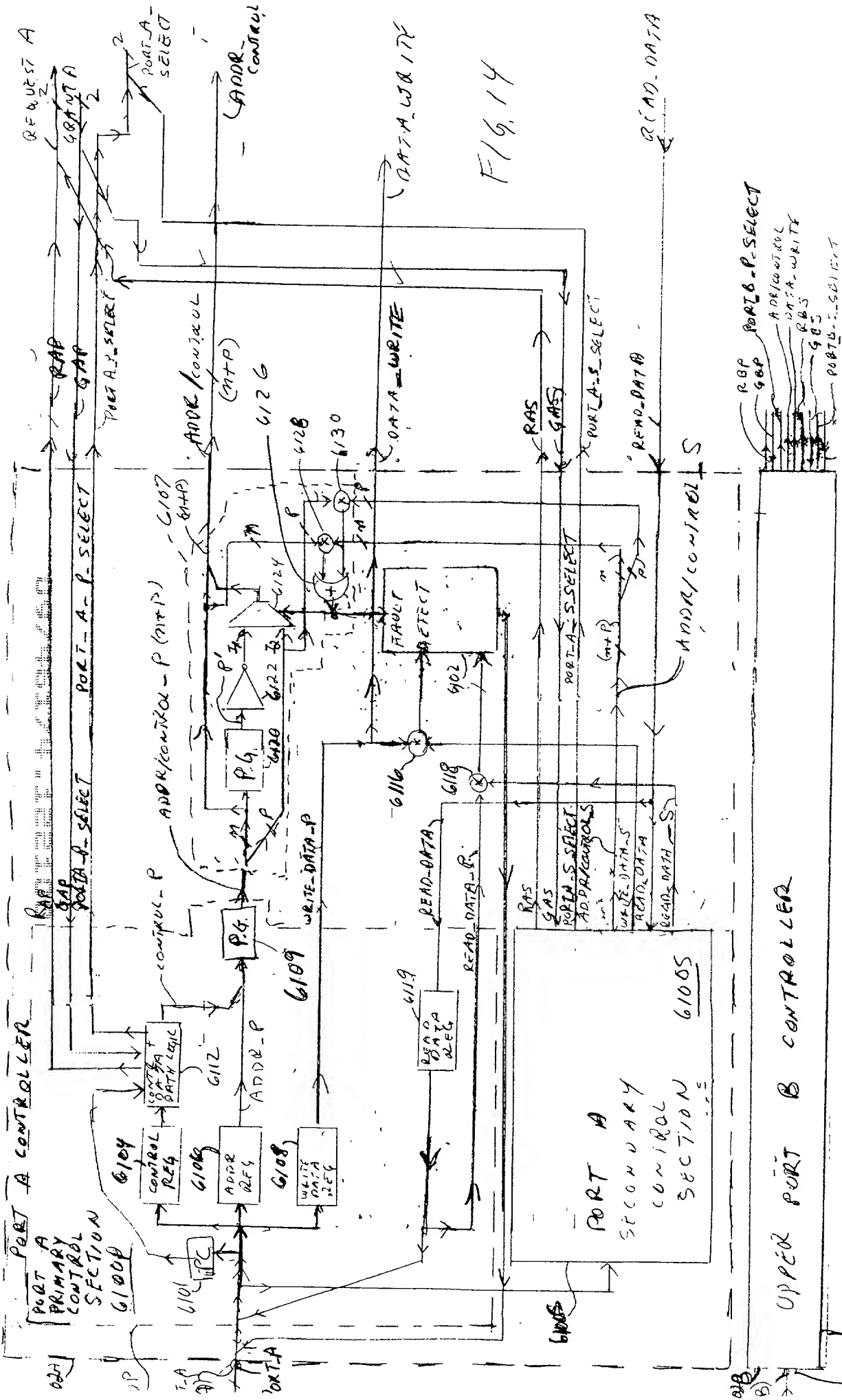


FIG. 10

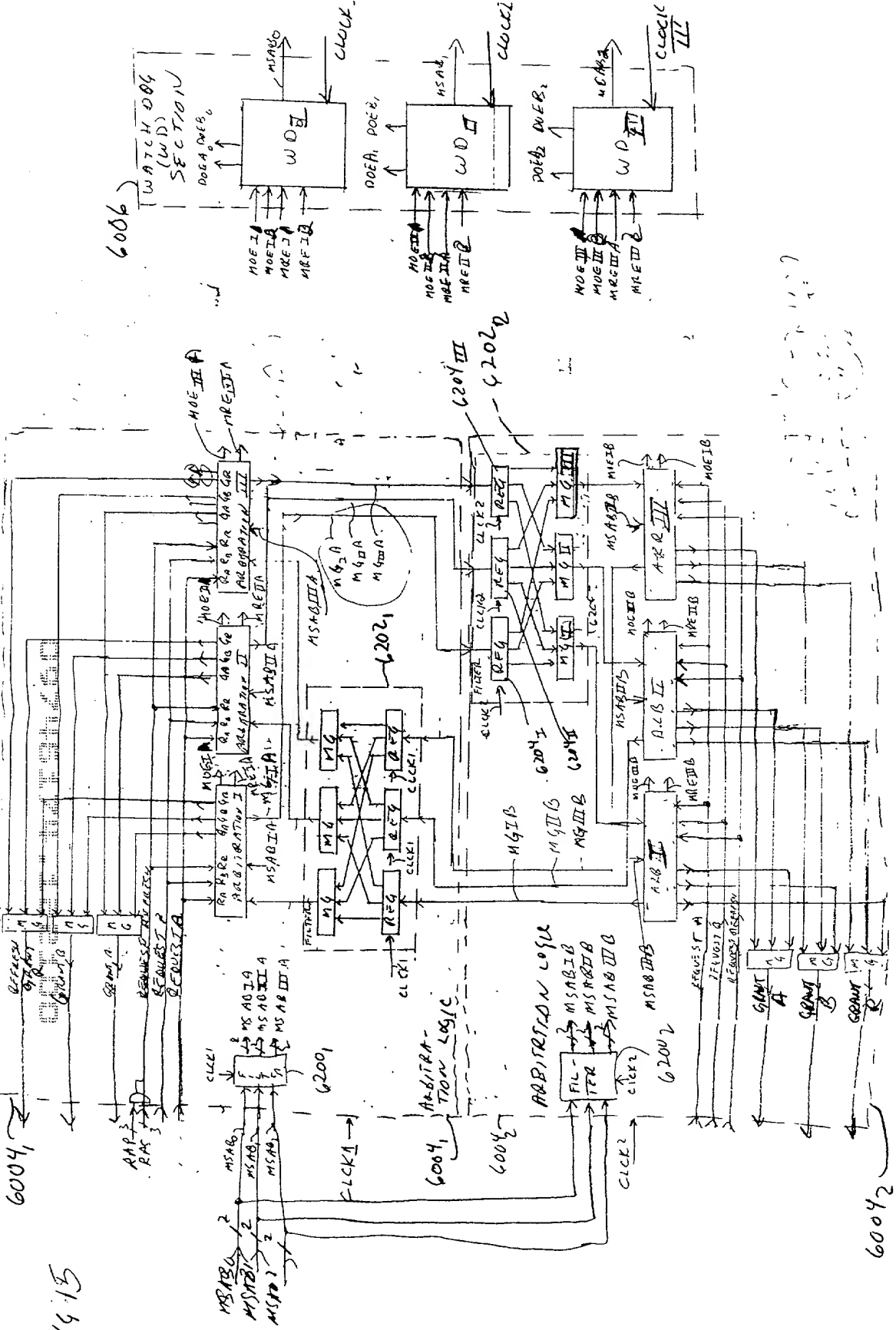


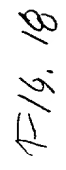
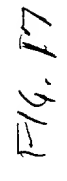
[illegible]



6002B
PORT B

FIG 15





F64 20

